



**BUREAU
VERITAS**

Certificate of compliance

Applicant: **SolarEdge Technologies Ltd.**
1 HaMada Street
Herzeliya 4673335
Israel

Product: **Grid-tied photovoltaic (PV) inverter**

Model: **SE2200H
SE3000H
SE3500H
SE3680H**

Use in accordance with regulations:

Automatic disconnection device with single-phase mains surveillance in accordance with Engineering Recommendation G98/1 for photovoltaic systems with a single-phase parallel coupling via an inverter in the public mains supply. The automatic disconnection device is an integral part of the aforementioned inverter. This serves as a replacement for the disconnection device with isolating function that can access the distribution network provider at any time.

Applied rules and standards:

Engineering Recommendation G98/1-4:2019

Requirements for the connection of Fully Type Tested Micro-generators (up to and including 16 A per phase) in parallel with public Low Voltage Distribution Networks

DIN V VDE V 0126-1-1:2006-02 (Functional safety)

Automatic disconnection device between a generator and the public low-voltage grid

At the time of issue of this certificate, the safety concept of an aforementioned representative product corresponds to the valid safety specifications for the specified use in accordance with regulations.

Certification program: **NSOP-0032-DEU-ZE-V01**

Report number: **16TH0371-G98/1_4**

Certificate number: **U20-0161**

Date of issue: **2020-03-18**

Certification body



Holger Schaffer

Certification body of Bureau Veritas Consumer Products Services Germany GmbH
Accredited according to DIN EN ISO/IEC 17065

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Type Approval and declaration of compliance with the requirements of Engineering Recommendation G98				
PGM Technology	Photovoltaic inverter			
Manufacturer:	SolarEdge Technologies Ltd.			
Address	1 HaMada Street Herzeliya 4673335 Israel			
Tel	+972-9-957-6620	Fax	+972-9-957-6591	
Email	info@solaredge.com	Website	www.solaredge.com	
Rated values	SE2200H	SE3000H	SE3500H	SE3680H
Maximum rated capacity	2200W	3000W	3500W	3680W
Rated voltage	220/230 60Hz/50Hz			
Firmware version	Main DSP software version is 1.130 Aux DSP software version is 2.19			
Measurement period:	2018-04-18, 2019-01-10 to 2019-02-05			
Description of the structure of the power generation unit:				
The power generation unit is equipped with a PV and line-side EMC filter. The power generation unit has no galvanic isolation between DC input and AC output. Output switch-off is performed with single-fault tolerance based on two series-connected relays in line and neutral. This enables a safe disconnection of the power generation unit from the network in case of error.				
Differences between Generating Units:				
The inverters of the SExxxH series consist of the following power models: SE2200H, SE3000H, SE3500H and SE3680H. All the models use the same hardware and software. The different powers are realized by software derating. Therefore the test results of the SE3680H model are valid for the whole series. If additional tests are done on other models, the use model is stated.				
The above stated Generating Units are tested according the requirements in the Engineering Recommendation G98/1. Any modification that affects the stated tests must be named by the manufacturer/supplier of the product to ensure that the product meets all requirements of the Engineering Recommendation G98/1.				

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Operating Range.	
Connection:	Always connected
Limit:	Always connected
Test 1	Voltage = 85% of nominal (195,5 V) Frequency = 47,5 Hz Power Factor = 1 Period of test 90 minutes
Connection:	Always connected
Limit:	Always connected
Test 2	Voltage = 110% of nominal (253 V) Frequency = 51,5 Hz Power Factor = 1 Period of test 90 minutes
Connection:	Always connected
Limit:	Always connected
Test 3	Voltage = 110% of nominal (253 V) Frequency = 52,0 Hz Power Factor = 1 Period of test 15 minutes
Connection:	Always connected
Limit:	Always connected

Protection. Voltage tests.						
Function	Setting		Trip test		No trip test	
	Voltage [V]	Time delay [s]	Voltage [V]	Time delay [s]	Voltage / time	Confirm no trip
U/V	184	2,5	184,6	2,757	188V / 5s	No trip
					180V / 2,45s	No trip
O/V stage 1	262,2	1,0	262,5	1,257	258,2V / 5,0s	No trip
O/V stage 2	273,7	0,5	273,5	0,757	269,7V / 0,95s	No trip
					277,7V / 0,45s	No trip

Note. For Voltage tests the Voltage required to trip is the setting $\pm 3,45V$. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting $\pm 4V$ and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Protection. Frequency tests.						
Function	Setting		Trip test		No trip test	
	Frequency [Hz]	Time delay [s]	Frequency [Hz]	Time delay [s]	Frequency / time	Confirm no trip
U/F stage 1	47,5	20	47,50	20,272	47,7Hz / 30s	No trip
U/F stage 2	47	0,5	47,00	0,808	47,2Hz / 19,5s	No trip
					46,8Hz / 0,45s	No trip
O/F stage 2	52	0,5	52,01	0,783	51,8Hz / 120s	No trip
					52,2Hz / 0,45s	No trip

Note. For Frequency Trip tests the Frequency required to trip is the setting $\pm 0,1$ Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No-trip tests" need to be carried out at the setting $\pm 0,2$ Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection. Loss of Mains.						
Inverters tested according to BS EN 62116.						
Balancing load on islanded network	33% of -5% Q Test 22	66% of -5% Q Test 12	100% of -5% P Test 5	33% of +5% Q Test 31	66% of +5% Q Test 21	100% of +5% P Test 10
Trip time. Ph1 fuse removed [s]	217	35	162	43	96	240

Note. Trip time limit is 0,5s. For technologies which have a substantial shut down time this can be added to the 0,5s in establishing that the trip occurred in less than 0,5s maximum. Shut down time could therefore be up to 1,0s for these technologies.

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Protection. Re-connection timer.					
Test should prove that the reconnection sequence starts in no less than 20 seconds for restoration of voltage and frequency to within the stage 1 settings of table 10.5.7.1.					
Over Voltage					
Time delay setting		Measured delay			
20s		24,0s			
Under Voltage					
Time delay setting		Measured delay			
20s		22,0s			
Over Frequency					
Time delay setting		Measured delay			
20s		24,0s			
Under Frequency					
Time delay setting		Measured delay			
20s		24,0s			
		Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.			
		At 266,2V	At 180,0V	At 47,4Hz	At 52,1Hz
Confirmation that the Generating Unit does not re-connect.	No reconnection	No reconnection	No reconnection	No reconnection	No reconnection

Protection. Frequency change, Stability test.				
	Start Frequency [Hz]	Change	Test Duration	Confirm no trip
Positive Vector Shift	49,5	+50 degrees		No trip
Negative Vector Shift	50,5	-50 degrees		No trip
Positive Frequency drift	49,0 to 51,0	+0,95Hz/sec	2,1s	No trip
Negative Frequency drift	51,0 to 49,0	-0,95Hz/sec	2,1s	No trip

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Limited Frequency Sensitive Mode – Over Frequency							
1-min mean value [Hz]:	a) 50,00	b) 50,45	c) 50,70	d) 51,15	e) 50,70	f) 50,45	g) 50,00
1. Measurement a) to g): Active power output > 80% Pn							
Frequency [Hz]:	50,00	50,45	50,70	51,15	50,70	50,45	50,00
PM [kW]:	N/A	3,62	3,44	3,11	3,44	3,62	N/A
PE60 [kW]:	3,66	3,62	3,44	3,11	3,44	3,62	3,66
Δ PE60/PM [%]:	N/A	0,00	0,00	0,00	0,00	0,00	N/A
Limit Δ P/P _{1min} :	+ 10 % of PM						
2. Measurement a) to g): Active power output 40% and 60% after freezing > 80% Pn							
Frequency [Hz]:	50,00	50,45	50,70	51,15	50,70	50,45	50,00
PM [kW]:	N/A	1,89	1,79	1,62	1,79	1,89	N/A
PE60 [kW]:	1,91	1,89	1,79	1,62	1,79	1,89	1,92
Δ PE60/PM [%]:	N/A	0,00	-0,00	-0,00	-0,00	0,00	N/A
Limit Δ P/P _{1min} :	+ 10 % of PM						

Output Power with falling Frequency			
5-min mean value (each)	a) 50 ± 0,01 Hz	b) - 0,4 to - 0,5 Hz	c) - 2,4 to - 2,5 Hz
Frequency [Hz]:	50,00	49,55	47,55
Active power [W]:	3597	3596	3598
Δ P/PM [%] per 1 Hz:			0

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Power Quality. Harmonics.						
SE2200H						
Phase 1						
SSEG rating per phase (rpp)						
	At 45-55% of rated output 1,21kW		100% of rated output 2,20kW			
Harmonic	Measured Value (MV) in [A]	Measured Value (%) in [A]	Measured Value (MV) in [A]	Measured Value (%) in [A]	Limit in BS EN61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2nd	0,011	0,064	0,021	0,119	1,080	
3rd	0,108	0,626	0,015	0,088	2,300	
4th	0,014	0,079	0,019	0,109	0,430	
5th	0,168	0,968	0,152	0,880	1,140	
6th	0,009	0,051	0,016	0,092	0,300	
7th	0,056	0,322	0,039	0,227	0,770	
8th	0,003	0,019	0,007	0,042	0,230	
9th	0,074	0,430	0,039	0,224	0,400	
10th	0,005	0,029	0,007	0,039	0,184	
11th	0,041	0,235	0,034	0,194	0,330	
12th	0,005	0,028	0,004	0,021	0,153	
13th	0,042	0,242	0,049	0,280	0,210	
14th	0,005	0,032	0,010	0,056	0,131	
15th	0,028	0,159	0,041	0,236	0,150	
16th	0,004	0,021	0,005	0,027	0,115	
17th	0,024	0,140	0,035	0,202	0,132	
18th	0,006	0,032	0,009	0,052	0,102	
19th	0,033	0,189	0,031	0,181	0,118	
20th	0,003	0,019	0,005	0,031	0,092	
21th	0,015	0,088	0,021	0,124	0,107	0,160
22th	0,006	0,037	0,010	0,056	0,084	
23th	0,008	0,047	0,024	0,137	0,098	0,147
24th	0,004	0,022	0,005	0,028	0,077	
25th	0,009	0,051	0,021	0,121	0,090	0,135
26th	0,007	0,043	0,007	0,042	0,071	
27th	0,012	0,068	0,021	0,119	0,083	0,124
28th	0,004	0,023	0,005	0,029	0,066	
29th	0,012	0,070	0,021	0,121	0,078	0,117
30th	0,007	0,038	0,005	0,027	0,061	
31th	0,011	0,065	0,016	0,094	0,073	0,109
32th	0,004	0,022	0,007	0,039	0,058	
33th	0,017	0,099	0,017	0,100	0,068	0,102
34th	0,006	0,035	0,005	0,030	0,054	
35th	0,016	0,094	0,012	0,070	0,064	0,096
36th	0,005	0,027	0,011	0,063	0,051	
37th	0,018	0,106	0,015	0,084	0,061	0,091
38th	0,007	0,040	0,006	0,032	0,048	
39th	0,009	0,053	0,015	0,089	0,058	0,087
40th	0,006	0,033	0,013	0,074	0,046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Power Quality. Harmonics.						
SE3680						
Phase 1						
SSEG rating per phase (rpp)						
	At 45-55% of rated output 1,84kW		100% of rated output 3,68kW			
Harmonic	Measured Value (MV) in [A]	Measured Value (%) in [A]	Measured Value (MV) in [A]	Measured Value (%) in [A]	Limit in BS EN61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2nd	0,021	0,119	0,047	0,271	1,080	
3rd	0,028	0,160	0,067	0,384	2,300	
4th	0,018	0,106	0,036	0,206	0,430	
5th	0,157	0,904	0,119	0,687	1,140	
6th	0,014	0,079	0,017	0,100	0,300	
7th	0,044	0,255	0,035	0,205	0,770	
8th	0,007	0,040	0,007	0,040	0,230	
9th	0,045	0,260	0,030	0,174	0,400	
10th	0,006	0,033	0,008	0,047	0,184	
11th	0,035	0,202	0,022	0,127	0,330	
12th	0,004	0,024	0,011	0,062	0,153	
13th	0,048	0,275	0,032	0,186	0,210	
14th	0,008	0,045	0,005	0,031	0,131	
15th	0,042	0,245	0,049	0,284	0,150	
16th	0,004	0,024	0,011	0,065	0,115	
17th	0,033	0,191	0,032	0,186	0,132	
18th	0,007	0,038	0,007	0,042	0,102	
19th	0,031	0,179	0,032	0,188	0,118	
20th	0,004	0,023	0,012	0,067	0,092	
21th	0,022	0,125	0,028	0,161	0,107	0,160
22th	0,006	0,037	0,008	0,044	0,084	
23th	0,020	0,115	0,027	0,155	0,098	0,147
24th	0,005	0,027	0,012	0,068	0,077	
25th	0,019	0,112	0,033	0,190	0,090	0,135
26th	0,005	0,028	0,008	0,048	0,071	
27th	0,017	0,098	0,030	0,172	0,083	0,124
28th	0,007	0,042	0,015	0,086	0,066	
29th	0,023	0,134	0,031	0,178	0,078	0,117
30th	0,005	0,027	0,010	0,055	0,061	
31th	0,014	0,082	0,023	0,130	0,073	0,109
32th	0,009	0,049	0,014	0,079	0,058	
33th	0,018	0,101	0,026	0,149	0,068	0,102
34th	0,006	0,033	0,008	0,047	0,054	
35th	0,012	0,069	0,023	0,134	0,064	0,096
36th	0,012	0,067	0,014	0,080	0,051	
37th	0,013	0,078	0,023	0,131	0,061	0,091
38th	0,005	0,032	0,006	0,033	0,048	
39th	0,015	0,089	0,017	0,098	0,058	0,087
40th	0,011	0,066	0,009	0,053	0,046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Power Quality. Power factor.				
Output power	216,2V	230V	253V	Measured at three voltage levels and at full output. Voltage to be maintained within $\pm 1,5\%$ of the stated level during the test.
20%	0,999	0,999	0,999	
50%	0,999	0,999	0,999	
75%	0,999	0,999	0,999	
100%	0,999	0,999	0,999	
Limit	>0,95	>0,95	>0,95	

Power Quality. Voltage fluctuation and Flicker.								
	Starting			Stopping			Running	
	dmax	dc	d(t)	dmax	dc	d(t)	Pst	Plt 2 hours
Measured values at test impedance	1,113%	0,972%	0,00%	1,113%	0,972%	0,00%	0,214	0,132
Values at standard impedance	1,113%	0,972%	0,00%	1,113%	0,972%	0,00%	0,214	0,132
Limits set under BS EN 61000-3-11	4%	3,3%	3,3% 500ms	4%	3,3%	3,3% 500ms	1,0	0,65
Test impedance	R	0,4	Ω	XI	0,25	Ω		
	Z	0,472	Ω					
Standard impedance	R	0,4	Ω	XI	0,25	Ω		
	Z	0,472	Ω					

Power Quality. DC injection.				
SE2200H				
Test level power [%]	20	50	75	100
Recorded value [mA]	-16,31	-21,16	-18,45	-19,89
Recorded value [%]	-0,16	-0,21	-0,18	-0,20
Limit [%]	0,25	0,25	0,25	0,25
SE3680H				
Test level power [%]	20	50	75	100
Recorded value [mA]	5,49	4,00	2,79	2,93
Recorded value [%]	0,04	0,03	0,02	0,02
Limit [%]	0,25	0,25	0,25	0,25

Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98

Nr. 16TH0371-G98/1_4

Fault level Contribution.					
For a directly coupled SSEG			For a Inverter SSEG		
Parameter	Symbol	Value	Time after fault	Volts [V]	Amps [A]
Peak Short Circuit current	I_p	N/A	20ms	30,1	5,9
Initial Value of aperiodic current	A	N/A	100ms	24,2	5,7
Initial symmetrical short-circuit current*	I_k	N/A	250ms	11,2	5,0
Decaying (aperiodic) component of short circuit current*	i_{DC}	N/A	500ms	8,2	2,8
Reactance/Resistance Ratio of source*	X/R	N/A	Time to Trip [s]	0,391	

For rotating machines and linear piston machines the test should produce a 0s – 2s plot of the short circuit current as seen at the Generating Unit terminals.

* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot.

Self Monitoring – Solid state switching.	N/A
It has been verified that in the event of the solid state switching device failing to disconnect the Power Park Module, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0,5 seconds.	N/A (No solid state switching device)
Note. Unit do not provide solid state switching relays. In case the semiconductor bridge is switched off, then the voltage on the output drops to 0. In this case, the relays on the output will also open (Functional safety of the internal automatic disconnection device according to VDE 0126-1-1).	

Logic Interface (input port) Required by paragraph 11.1.3	P
Confirm that an input port is provided and can be used to shut down the module.	Yes