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# G83/2 Appendix 4 Type Verification Test Report

Type Approval and manufacturer/supplier declaration of compliance with the reconfigure of Engineering Recommendation G83/2.										
SSEG Type reference number			Photovolta	aic Grid-tied	inverter					
SSEG Type			X1-AC-3.0	) ; X1-AC-3.6	5					
System Supp	plier name		Solax pov	ver Co., Ltd						
Address			University Zhejiang	Room 220, West Building A, National University Science and Technology Park of Zhejiang University 525, Xixi Rd, Hangzhou, Zhejiang Province, China,310007						
Tel	+86(0571	)-56260011		Fax	+86(0571)-56075753					
E:mail	info@sola	axpower.com		Web site	www.solaxpower.com					
			Connection Option							
		3.0	kW single phase system							
Maximum ra capacity, use	e separate	3.6	kW single phase system							
sheet if more connection of		NA	kW two ph	ases in three <sub>l</sub>	ohase system					
		NA	kW two phases split phase system							
SSEG manufacturer/supplier declaration.  I certify on behalf of the company named above as a manufacturer/supplier of Sr Embedded Generators, that all products manufactured/supplied by the company above SSEG Type reference number will be manufactured and tested to ensure perform as stated in this Type Verification Test Report, prior to shipment to site as site modifications are required to ensure that the product meets all the require G83/2.  Signed  On behalf of  Solax power Co.,										
Signed	Guo	Huawei	On benall	UI	Solax power Co., Ltu					

Note that testing can be done by the manufacturer of an individual component, by an external test house, or by the supplier of the complete system, or any combination of them as appropriate.

Where parts of the testing are carried out by persons or organisations other than the supplier then the supplier shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



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MATERIALS & SAFETY – R&D TR180904S Page 2 of 7 <b>Power Quality. Harmonics</b> . The requirement is specified in section 5.4.1, test procedure.								
Power Quin Annex A		onics. The re	equirement is	specified in s	section 5.4	1.1, test procedure		
SSEG	rating per ph	ase (rpp)	3.6	kW	NV=	=MV*3.68/rpp		
Harmonic	At 45-55	5% of rated	100% of r	ated output				
		utput Normalised	Magazirad	Normalised	Limit in	I liabar limit for		
	Measured Value	Value	Measured Value	Value	Limit in BS EN	Higher limit for odd harmonics		
	(MV) in	(NV) in	(MV) in	(NV) in	61000-	21 and above		
	Amps	Amps	Amps	Amps	3-2 in	Zi diid dbovo		
		, -			Amps			
2	0.0616	0.0629	0.1280	0.1308	1.0800			
3	0.0867	0.0886	0.2189	0.2237	2.3000			
4	0.0056	0.0057	0.0094	0.0096	0.4300			
5	0.0188	0.0192	0.0458	0.0468	1.1400			
6	0.0056	0.0057	0.0036	0.0036	0.3000			
7	0.0259	0.0264	0.0349	0.0356	0.7700			
8	0.0077	0.0078	0.0077	0.0078	0.2300			
9	0.0221	0.0225	0.0293	0.0299	0.4000			
10	0.0068	0.0069	0.0063	0.0064	0.1840			
11	0.0211	0.0215	0.0270	0.0276	0.3300			
12	0.0053	0.0054	0.0067	0.0068	0.1530			
13	0.0221	0.0225	0.0241	0.0246	0.2100			
14	0.0056	0.0057	0.0042	0.0042	0.1310			
15	0.0203	0.0207	0.0240	0.0245	0.1500			
16	0.0070	0.0071	0.0067	0.0068	0.1150			
17	0.0203	0.0207	0.0195	0.0199	0.1320			
18	0.0050	0.0051	0.0071	0.0072	0.1020			
19	0.0146	0.0149	0.0165	0.0168	0.1180			
20	0.0040	0.0040	0.0044	0.0044	0.0920			
21	0.0148	0.0151	0.0163	0.0166	0.1070	0.160		
22	0.0051	0.0052	0.0039	0.0039	0.0840			
23	0.0134	0.0136	0.0134	0.0136	0.0980	0.147		
24	0.0036	0.0036	0.0042	0.0042	0.0770			
25	0.0128	0.0130	0.0130	0.0132	0.0900	0.135		
26	0.0034	0.0034	0.0040	0.0040	0.0710			
27	0.0117	0.0119	0.0107	0.0109	0.0830	0.124		
28	0.0036	0.0036	0.0037	0.0037	0.0660			
29	0.0107	0.0109	0.0098	0.0100	0.0780	0.117		
30	0.0022	0.0022	0.0034	0.0034	0.0610			
31	0.0097	0.0099	0.0114	0.0116	0.0730	0.109		
32	0.0030	0.0030	0.0038	0.0038	0.0580			
33	0.0092	0.0094	0.0084	0.0085	0.0680	0.102		
34	0.0023	0.0023	0.0038	0.0038	0.0540			
35	0.0078	0.0079	0.0059	0.0060	0.0640	0.096		
36	0.0020	0.0020	0.0031	0.0031	0.0510			
37	0.0099	0.0101	0.0103	0.0105	0.0610	0.091		
38	0.0020	0.0020	0.0027	0.0027	0.0480	0.007		
39	0.0093	0.0095	0.0138	0.0141	0.0580	0.087		
40	0.0022	0.0022	0.0026	0.0026	0.0460			



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Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

**Power Quality.** Harmonics. The requirement is specified in section 5.4.1, test procedure in Annex A or B 1.4.1

	SSEG rating per phase (rpp)		3.0	kW	NV=MV*3.68/rpp	
Harmonic		6% of rated		ated output		отостър
		ıtput				
	Measured	Normalised	Measured	Normalised	Limit in	Higher limit for
	Value	Value	Value	Value	BS EN	odd harmonics
	(MV) in	(NV) in	(MV) in	(NV) in	61000-	21 and above
	Amps	Amps	Amps	Amps	3-2 in	
2	0.0445	0.0545	0.0967	0.1186	Amps 1.0800	
3	0.0443	0.0343	0.0907	0.1100	2.3000	
4	0.0730	0.0970	0.1363	0.1919	0.4300	
5	0.0034	0.0041	0.0009	0.0064	1.1400	
6		0.0223	0.0292	0.0055		
7	0.0060				0.3000	
	0.0255	0.0312	0.0333	0.0408	0.7700	
8	0.0079	0.0096	0.0083	0.0101	0.2300	
9	0.0208	0.0255	0.0244	0.0299	0.4000	
10	0.0083	0.0101	0.0093	0.0114	0.1840	
11	0.0191	0.0234	0.0229	0.0280	0.3300	
12	0.0059	0.0072	0.0060	0.0073	0.1530	
13	0.0232	0.0284	0.0230	0.0282	0.2100	
14	0.0053	0.0065	0.0042	0.0051	0.1310	
15	0.0177	0.0217	0.0190	0.0233	0.1500	
16	0.0055	0.0067	0.0050	0.0061	0.1150	
17	0.0147	0.0180	0.0179	0.0219	0.1320	
18	0.0067	0.0082	0.0094	0.0115	0.1020	
19	0.0137	0.0168	0.0145	0.0177	0.1180	
20	0.0050	0.0061	0.0049	0.0060	0.0920	
21	0.0150	0.0184	0.0155	0.0190	0.1070	0.160
22	0.0053	0.0065	0.0052	0.0063	0.0840	
23	0.0125	0.0153	0.0129	0.0158	0.0980	0.147
24	0.0037	0.0045	0.0051	0.0062	0.0770	
25	0.0114	0.0139	0.0116	0.0142	0.0900	0.135
26	0.0035	0.0042	0.0048	0.0058	0.0710	
27	0.0096	0.0117	0.0091	0.0111	0.0830	0.124
28	0.0039	0.0047	0.0048	0.0058	0.0660	
29	0.0088	0.0107	0.0098	0.0120	0.0780	0.117
30	0.0019	0.0023	0.0042	0.0051	0.0610	
31	0.0090	0.01104	0.0088	0.0107	0.0730	0.109
32	0.0031	0.0038	0.0042	0.0051	0.0580	
33	0.0072	0.0088	0.0079	0.0096	0.0680	0.102
34	0.0026	0.0031	0.0038	0.0046	0.0540	
35	0.0063	0.0077	0.0049	0.0060	0.0640	0.096
36	0.0023	0.0028	0.0032	0.0039	0.0510	



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37	0.0094	0.0115	0.0061	0.0074	0.0610	0.091
38	0.0024	0.0029	0.0030	0.0036	0.0480	
39	0.0094	0.0115	0.0064	0.0078	0.0580	0.087
40	0.0027	0.0033	0.0028	0.0034	0.0460	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

**Power Quality. Voltage fluctuations and Flicker**. The requirement is specified in section 5.4.2, test procedure in Annex A or B 1.4.3

	Starting			Stoppin	Stopping			Running	
	d <sub>max</sub>	d <sub>c</sub>	d <sub>(t)</sub>	d <sub>max</sub>	d <sub>c</sub>	d <sub>(t)</sub>	P <sub>st</sub>	P <sub>lt</sub> 2 hours	
Measured Values	0.63%	0.04%	0%	0.1%	0.09%	0%	0.29	0.16	
Normalised to standard impedance and 3.68kW for multiple units	NA	NA	NA	NA	NA	NA	NA	NA	
Limits set under BS EN 61000-3-2	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65	
Test start date		2017-05	-10	Test end d	late	20	17-05-10	Ö	
Test location			•	. 518, Xin rk, Shang				Songjiang	

_	<b>Power quality. DC injection.</b> The requirement is specified in section 5.5, test procedure in Annex A or B 1.4.4								
Allilex A Of L	3.6K								
Test power level	10%	55%	100%						
Recorded value	12.9mA	18mA	20.1mA						
as % of rated AC current	0.07%	0.10%	0.122%						
Limit	0.25%	0.25%	0.25%						

Power quali	ty. DC injection.	The requirement is specif	fied in section 5.5, test p	rocedure in					
Annex A or E	Annex A or B 1.4.4								
		3.0K							
Test power level	10%	55%	100%						
Recorded value	20.2mA	14.2 mA	27.2 mA						
as % of rated AC current	0.15%	0.10%	0.20%						
Limit	0.25%	0.25%	0.25%						



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	<b>Power Quality. Power factor</b> . The requirement is specified in section 5.6, test procedure in Annex A or B 1.4.2							
			3.6K					
	216.2V	230V	253V	Measured at three voltage levels and at full output. Voltage to be maintained				
Measured value	0.998	0.998	0.998	within ±1.5% of the stated level during the test.				
Limit	>0.95	>0.95	>0.95					

	<b>Power Quality. Power factor</b> . The requirement is specified in section 5.6, test procedure in Annex A or B 1.4.2							
			3.0k					
	216.2V	230V	253V	Measured at three voltage levels and at full output. Voltage to be maintained				
Measured value	0.998	0.998	0.998	within ±1.5% of the stated level during the test.				
Limit	>0.95	>0.95	>0.95					

	Protection. Frequency tests The requirement is specified in section 5.3.1, test procedure							
in Annex A or B 1.3.3								
Function	Setting		Trip test		"No trip tests"			
	Frequency	Time	Frequency	Time	Frequency	Confirm no trip		
		delay	. ,	delay	/time			
U/F stage 1	47.5Hz	20s	47.5Hz	20.3s	47.7Hz	no trip		
					25s	·		
U/F stage 2	47Hz	0.5s	47.00Hz	0.586s	47.2Hz	no trip		
					19.98s	·		
					46.8Hz	no trip		
					0.48s			
O/F stage 1	51.5Hz	90s	51.5Hz	90.7s	51.3Hz	no trip		
					95s			
O/F stage 2	52Hz	0.5s	52Hz	0.56s	51.8Hz	no trip		
					89.98s			
					52.2Hz	no trip		
					0.48s	-		



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<b>Protection. Voltage tests</b> The requirement is specified in section 5.3.1, test procedure in Annex A or B 1.3.2							
Function	Setting		Trip test		"No trip tests"	,	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V stage 1	200.1V	2.5s	200.1V	2.6 s	204.1V 3.5s	no trip	
U/V stage 2	184V	0.5s	183.8V	0.59s	188V 2.48s	no trip	
					180V 0.48s	no trip	
O/V stage 1	262.2V	1.0s	262.2V	1.08 s	258.2V 2.0s	no trip	
O/V stage 2	273.7V	0.5s	272.6V	0.76s	269.7V 0.98s	no trip	
					277.7V 0.48s	no trip	

Note for Voltage tests the Voltage required to trip is the setting  $\pm 3.45$ V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting  $\pm 4$ V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

**Protection.** Loss of Mains test and single phase test. The tests are to be carried out at three output power levels plus or minus 5%, an alternative for inverter connected Generating Units can be used instead.

Note as an alternative, inverters can be tested to BS EN 62116. The following sub set of tests should be recorded in the following table.

Test Power	33%	66%	100%	33%	66%	100%
and	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5%
imbalance	Test 22	Test 12	Test 5	Test 31	Test 21	Р
Trip time. Limit is 0.5s	0.448s	0.156s	0.308s	0.166s	0.149s	0.220s

<b>Protection.</b> Frequency change, Stability test The requirement is specified in section 5.3.3, test procedure in Annex A or B 1.3.6						
	Start Frequency	Change	End Frequency	Confirm no trip		
Positive Vector Shift	49.5Hz	+9 degrees		no trip		
Negative Vector Shift	50.5Hz	- 9 degrees		no trip		
Positive Frequency drift	49.5Hz	+0.19Hz/sec	51.5Hz	no trip		
Negative Frequency drift	50.5Hz	-0.19Hz/sec	47.5Hz	no trip		



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Protection. Re-connection timer. The requirement is specified in section 5.3.4, test						
procedure in Annex A or B 1.3.5						
Test should prove that the reconnection sequence starts after a minimum delay of 20 seconds for restoration of voltage and frequency to within the stage 1 settings of table 1.						
Time delay setting	Measur ed delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.				
60s	74.8s	At 266.2V	At 196.1V	At 47.4Hz	At 51.6Hz	
Confirmation SSEG does connect.		No-reconnection	No-reconnection	No-reconnection	No-reconnection	

Fault level contribution. T	he requirer	ment is s	pecified in sect	ion 5.7, tes	st procedure in
Annex A or B 1.4.6					
For a directly coupled SSEG			For a Inverter SSEG		
Parameter	Symbol	Value	Time after	Volts	Amps
			fault		
Peak Short Circuit current	i <sub>p</sub>	NA	20ms	155V	28.6A
Initial Value of aperiodic	Α	NA	100ms	NA	NA
current					
Initial symmetrical short-	$I_k$	NA	250ms	NA	NA
circuit current*					
Decaying (aperiodic)	$i_{DC}$	NA	500ms	NA	NA
component of short circuit					
current*					
Reactance/Resistance	$^{\times}/_{R}$	NA	Time to trip	0.557s	In seconds
Ratio of source*					

<b>Self-Monitoring solid state switching</b> The requirement is specified in section 5.3.1, No specified test requirements.	Yes/or NA
3.0/3.6KW	NA
It has been verified that in the event of the solid state switching device failing to disconnect the SSEG, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 seconds.	

Additional comments		